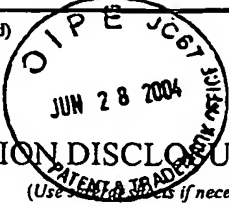


Form PTO-1449 (Modified)		U.S. Department of Commerce Patent and Trademark Office	Arty. Docket No. Intel/17880	Serial No. 10/747,764
<b>INFORMATION DISCLOSURE STATEMENT</b> <small>(Use separate sheets if necessary)</small>		Applicants Patel et al.		
		Filing Date December 29, 2003	Group Art Unit <del>2183</del> 2189	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)		
RIE	C01	P6 Family of Processors: Hardware Developer's Manual, Intel Corporation, September 1998, 16 pages
RIE	C02	IS 32 Intel Architecture Software Developer's Manual, Volume 3: System Programming Guide, Intel Corporation, 2003, Chapter 2: Sytem Architecture Overview, pp. 2.1-2.7
RIE	C03	IS 32 Intel Architecture Software Developer's Manual, Volume 3: System Programming Guide, Intel Corporation, 2003, Chapter 3: Protected-Mode Memory Management, pp. 3.1-3.38
RIE	C04	IA-32 Intel® Architecture Optimization: Reference Manual, Intel Corporation, 2003, Chapter 1: IA-32 Intel® Architecture Processor Family Overview, pp. 1.1- 1-34
RIE	C05	IA-32 Intel® Architecture Software Developer's Manual, Volume 1: Basic Architecture, Intel Corporation, 2003, Chapter 3: Basic Execution Environment, pp. 3.1-3.17

EXAMINER R. Elmore	DATE CONSIDERED 6-3-2006
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance <u>and</u> not considered. Include copy of this form with next communication to applicant.	